

TO-247-3

SiC Power MOSFETs

CoolCAD Power MOSFETs exceed power, efficiency and portability capabilities of standard silicon devices and are available in a variety of breakdown voltages (650V, 1200V, 1700V & 3300V) and current ratings. They have low on-resistance and low leakage in the blocking state. **Fabricated** on high-quality SiC epitaxial layers, our proprietary fabrication process includes carefully chosen annealing procedures to ensure a high-quality SiC-SiO₂ gate oxide dielectric layer. Doping profile, neck region, and edge termination ensure extremely low Ron and high breakdown voltage.

BENEFITS

Higher efficiency

Reduced cooling

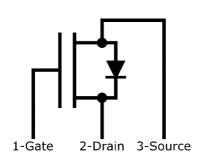
Increased power

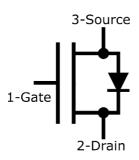
Reduced system volume

APPLICATIONS INCLUDE

Electromechanical power converters, DC to DC, AC to DC and DC to AC converters, switching power supplies, electric vehicles, hybrid vehicles, solar and wind energy power converters.







Part Number	Package	Marking
CC-CL-75-1023	TO-247-3	CoolCADElectronics







^{*} For description only. No rights are granted. No liability is assumed for choice of products.

Maximum Ratings						
*Characteristics	Symbol	Comments	Min	Тур	Max	Units
			,			
DC blocking voltage	V _{DSmax}	T _J =25°C to 175°C	1200			V
Gate input voltage range		Recommended range	-5		20	
	V _{GS}	Dynamic	-7		22	V
Avalanche rating	\ \ <u>\</u>	$V_{GS}=0V; I_{DS}=100\mu A; T_{J}=25^{\circ}C$	1200		1550	V
	V _{AVA}	V _{GS} =0V; I _{DS} =100μA; Τ _J =175°C			1620	
		V _{GS} =20V; T _J =25°C		100		
Pulsed drain current	I _{Dpulsed}	V _{GS} =20V; T _J =100°C		80		Α
		limited by Tj, tp=300μs		00		A
Continuous drain current	1 .	V _{GS} =20V; T _J =25°C			33	A
	I _D	V _{GS} =20V; T _J =100°C		23	^	
Continuous drain power	Р	V _{GS} =20V; T _J =25°C			135	W
Maximum- junction temperature	_	Normal operation			175	*0
	T _{jmax}	During processing / soldering			250	°C

Electrical and Thermal Characteristics						
*Characteristics	Symbol	Comments	Min	Тур	Max	Units
Gate threshold voltage	V_{TH}	$V_{GS}=V_{DS}$; $I_{DS}=10$ mA; $T_{J}=25$ °C	2.4	2.65	2.9	V
	V _{TH}	$V_{GS}=V_{DS}$; $I_{DS}=10$ mA; $T_{J}=175$ °C		1.6		V
Gate leakage	I _{GSS}	V _{GS} =20V; V _{DS} =0; T _J =25°C		35	80	pА
	-033	V _{GS} =20V; V _{DS} =0; T _J =175°C		65	100	F
Drain leakage	I _{DSS}	V _{DS} =1000V; V _{GS} =0; T _J =25°C		0.1	10 20	μA
		V _{DS} =1000V; V _{GS} =0; T _J =175°C V _{GS} =20V; I _{DS} =10A; T _J =25°C	50	60	70	
		V _{GS} =20V; I _{DS} =10A; T _J =175°C		115		
Drain-source on-resistance	R _{DSON}	V _{GS} =15V; I _{DS} =10A; T _J =25°C		85		mΩ
		V _{GS} =15V; I _{DS} =10A; T _J =175°C		125		
	0	V _{DS} =10V; I _{DS} =20A; T _J =25°C		7.8		0
Transconductance	G _m	V _{DS} =10V; I _{DS} =20A; T _J =175°C		8.2		S
Input capacitance	C _{ISS}	V _{GS} =0V; V _{DS} =200 / 1000V		960/930		
Output capacitance	C _{OSS}	f=1MHz; T _i =25°C		95 / 55		pF
Reverse transfer capacitance	C _{RSS}	2, 13 20 0		12/8		
Stored energy at output	E _{oss}	Double integral of C _{OSS}		40		
Turn on switching energy	_	V_{GS} -4/19V; V_{DD} =800V; $R_{G(ext)}$ =0 Ω		595		μJ
(with body diode)	E _{ON}	I _{DS} =20A; L=180μH; T _J =25°C		393		
Turn off switching energy (with body diode)	E _{OFF}	Clamped inductive switching waveform test circuit: Figure 27		110		
Rise time	t _r	V_{GS} =-4/19V; V_{DD} =800V; $R_{G(ext)}$ =0 Ω		18		
Fall time	t _f	I _{DS} =20A; L=180µH; T _J =25°C Clamped inductive switching waveform test circuit:		15.5		ns
Turn off dolay time	t _{d(on)}	Figure 27 Relative to V _{DS} inductive load: Figure 26		47		
i urn off delay time	$t_{d(off)}$			35.5		
Gate Charge Q _G		V_{GS} =-4/18V; V_{DD} =800V; $R_{G(ext)}$ =0 Ω				
	Q_{G}	I_{DS} =16A; R_L =50 Ω ; I_{GS} =38mA; T_J =25°C Figure 28		90		nC
Internal gate resistance	R_G	f=1Mz; V _{AC} =25mV; T _J =25°C		10		Ω
Thermal resistance:Junction to Case	R _{JC}	, ,,,		1.1		°C/W
	00	2.3		-		





Body diode characteristics						
*Characteristics	Symbol	Comments	Min	Тур	Max	Units
Diode forward voltage V _F		I _F =3A; V _{GS} =0V; T _J =25°C		2.6		V
	V_{F}	I _F =3A; V _{GS} =0V; T _J =175°C		2.1		
		I _F =10A; V _{GS} =−4V; T _J =25°C		4.4		
		I _F =10A;V _{GS} =−4V; T _J =175°C		3.7		
Pulsed diode current	Pulsed diade current	$V_{GS}=0V; V_{DS}=-3V; T_{J}=25^{\circ}C$		5.6		A
r dised diode current	Is(pulsed)	$V_{GS}=0V; V_{DS}=-3V; T_{J}=175^{\circ}C$		9.1		Α
Reverse recovery time	t _{rr}	V _{DD} =800V; V _{GS} =-4V; I _{DS} =20A		39		ns
Reverse recovery charge	Q_{rr}	R _{G(ext)} =20Ω L=180μH di/dt=400A/μs Clamped inductive switching waveform test circuit: Figure 27		123		nC
Peak reverse recovery current	I _{RRM}			6.5		A

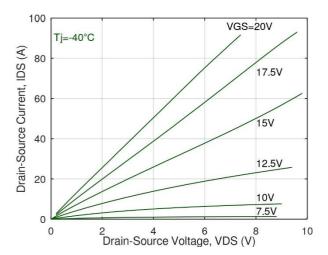


Figure 1: Low temperature output characteristics[†].

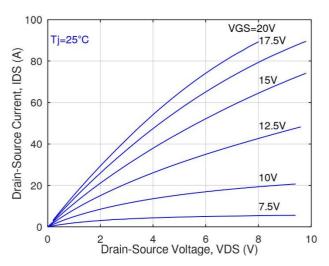


Figure 2: Room temperature output characteristics[†].

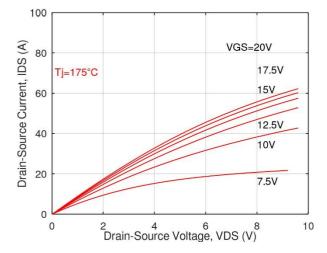


Figure 3: High temperature output characteristics[†].

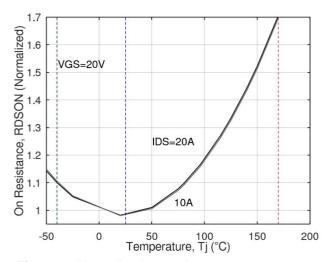


Figure 4: Normalized on-resistance vs. temperature. Dashed vertical lines indicate to room (25°C), high (175°C) and low (-40°C) temperatures.

Blue, red and green colors indicate data corresponding to room (25°C), high (175°C) and low (-40°C) temperatures, respectively. Unless stated otherwise, temperature corresponds to junction temperature.







[†] tp=300µs in pulsed IV measurements.

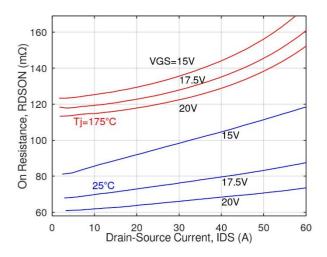


Figure 5: On-resistance vs. drain current.

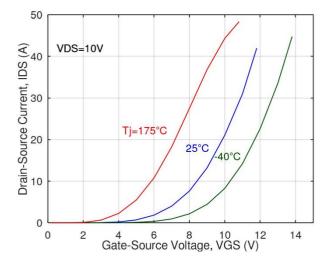
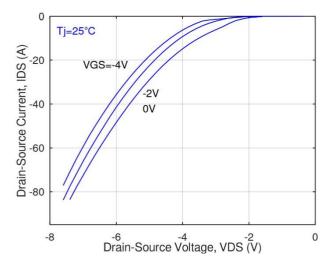


Figure 7: Transfer characteristics†.



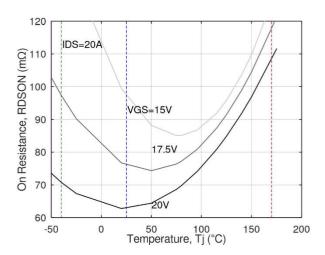


Figure 6: On-resistance vs. temperature. Dashed vertical lines indicate to room (25 $^{\circ}$ C), high (175 $^{\circ}$ C) and low (-40 $^{\circ}$ C) temperatures.

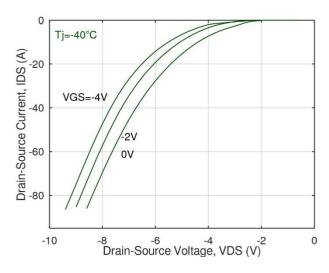


Figure 8: Low temperature body diode characteristics[†].

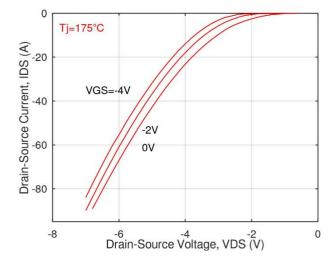


Figure 9: Room temperature body diode characteristics[†]. **Figure 10:** High temperature body diode characteristics[†].

Blue, red and green colors indicate data corresponding to room (25°C), high (175°C) and low (-40°C) temperatures, respectively. Unless stated otherwise, temperature corresponds to junction temperature.







[†] tp=300µs in pulsed IV measurements.

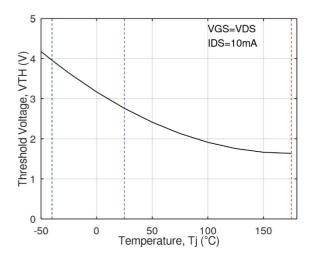
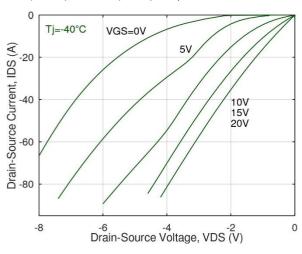


Figure 11: Threshold vs. temperature. Dashed vertical lines indicate to room (25°C), high (175°C) and low (-40°C) temperatures.

Figure 12: Gate charge characteristics.



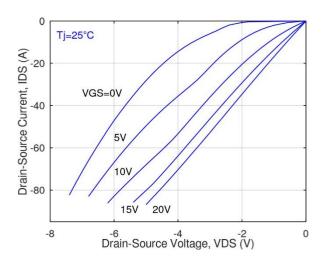
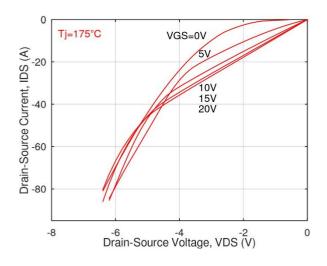


Figure 13: Low temperature third quadrant characteristics[†]. Figure 14: Room temperature third quadrant characteristics[†].



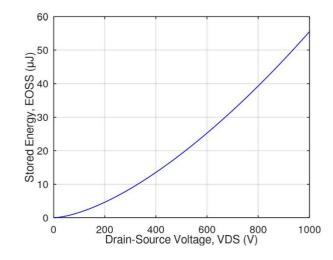


Figure 15: High temperature third quadrant characteristics[†].

Figure 16: Output capacitor stored energy.

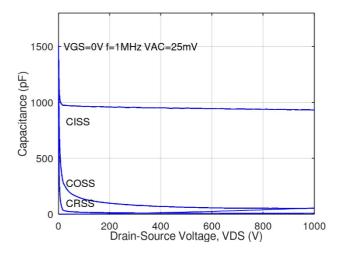
Blue, red and green colors indicate data corresponding to room (25°C), high (175°C) and low (-40°C) temperatures, respectively. Unless stated otherwise, temperature corresponds to junction temperature.







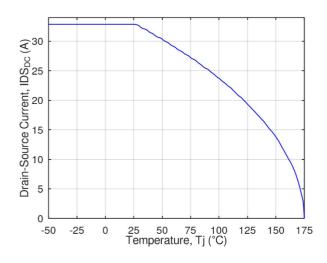
[†] tp=300µs in pulsed IV measurements.



1500 VGS=0V f=1MHz VAC=25mV Capacitance (pF) CISS 500 coss CRSS 0 0 50 100 150 Drain-Source Voltage, VDS (V) 200

Figure 17: Capacitance vs. drain voltage.

Figure 18: Capacitance vs. drain voltage.



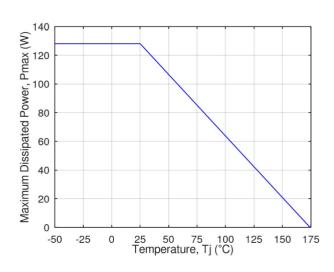
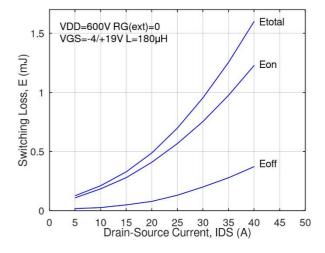


Figure 19: Continuous drain current vs. temperature.

Figure 20: Power dissipation derating vs. temperature.



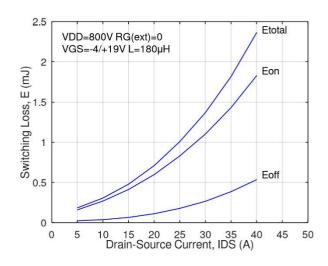


Figure 21: Clamped inductive switching energy vs. drain current at 600V VDD.

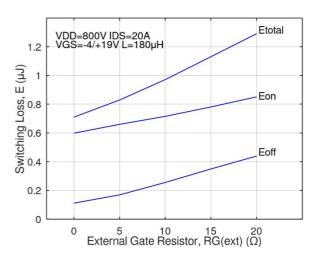
Figure 22: Clamped inductive switching energy vs. drain current at 800V VDD.

Blue color indicates data corresponding to room temperature measurements. Unless stated otherwise, temperature corresponds to junction temperature.









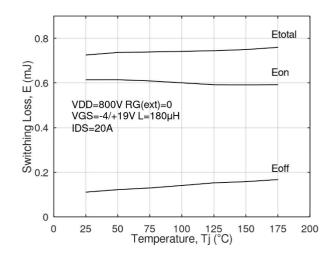
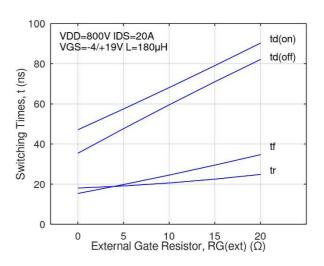


Figure 23: Clamped inductive switching energy vs. external Figure 24: Clamped inductive switching energy vs. temperature. gate resistance.



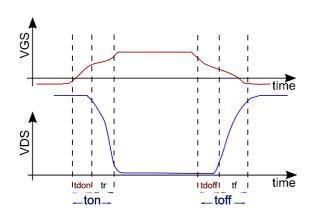


Figure 25: Switching times vs. external gate resistance.

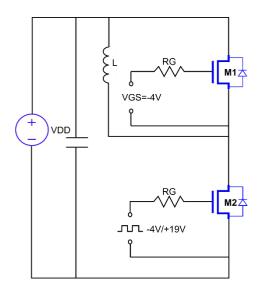


Figure 26: Timing references.

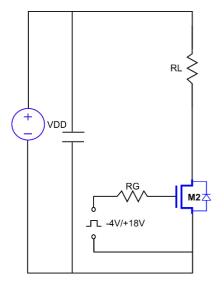


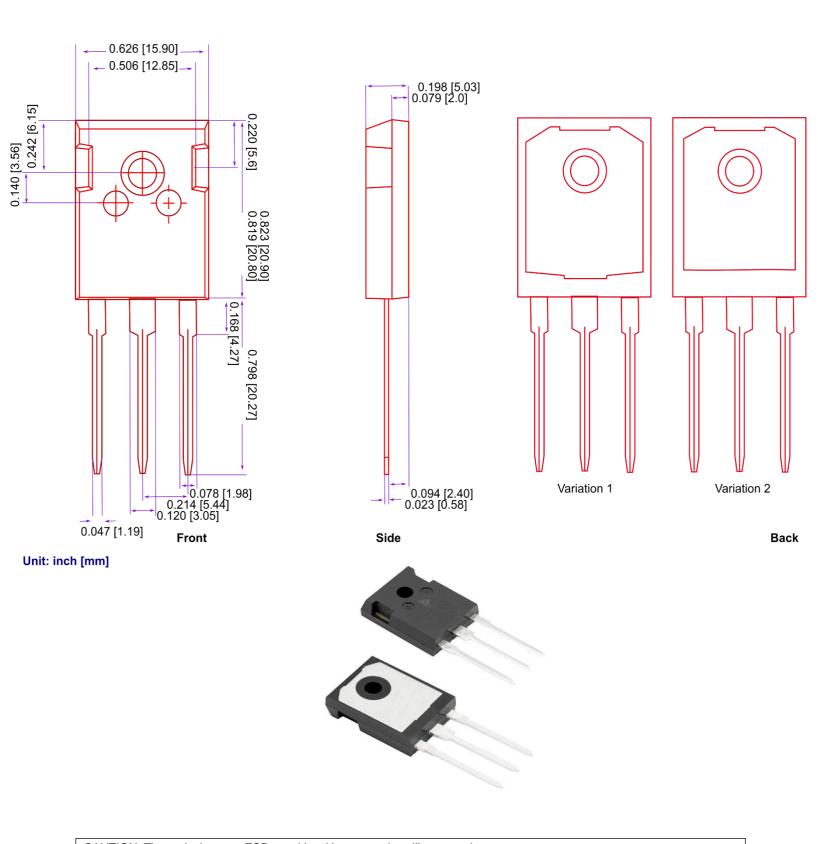
Figure 27: Clamped inductive switching waveform test circuit.

Figure 28: Gate charge test circuit.









CAUTION: These devices are ESD sensitive. Use proper handling procedures.

Disclaimer: These specifications may not be considered as a guarantee of components characteristics. Components have to be tested depending on intended application as adjustments may be necessary. The use of CoolCAD Electronics components in life support appliances and systems are subject to written approval of CoolCAD Electronics.

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